

FIG.1A

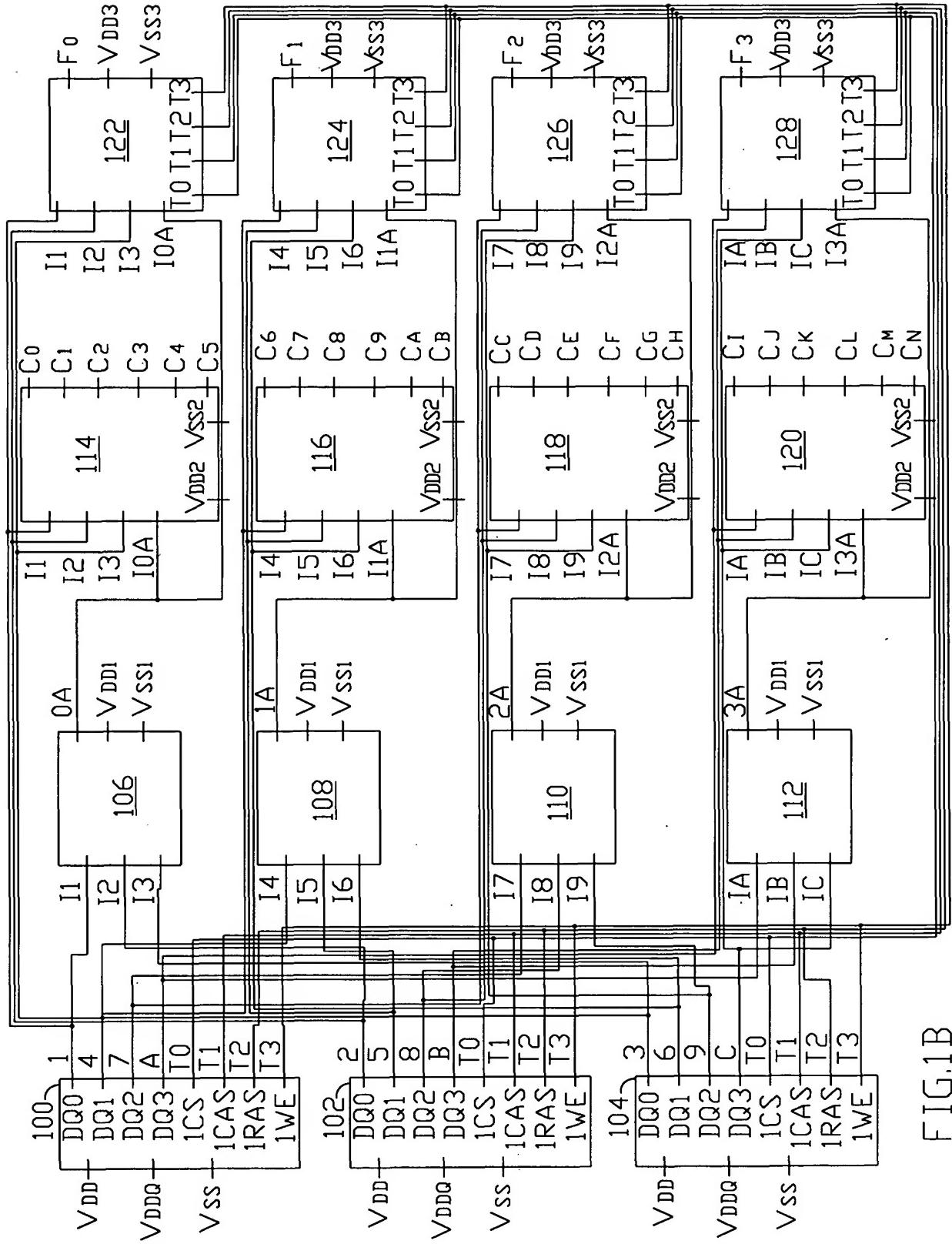


FIG.1B

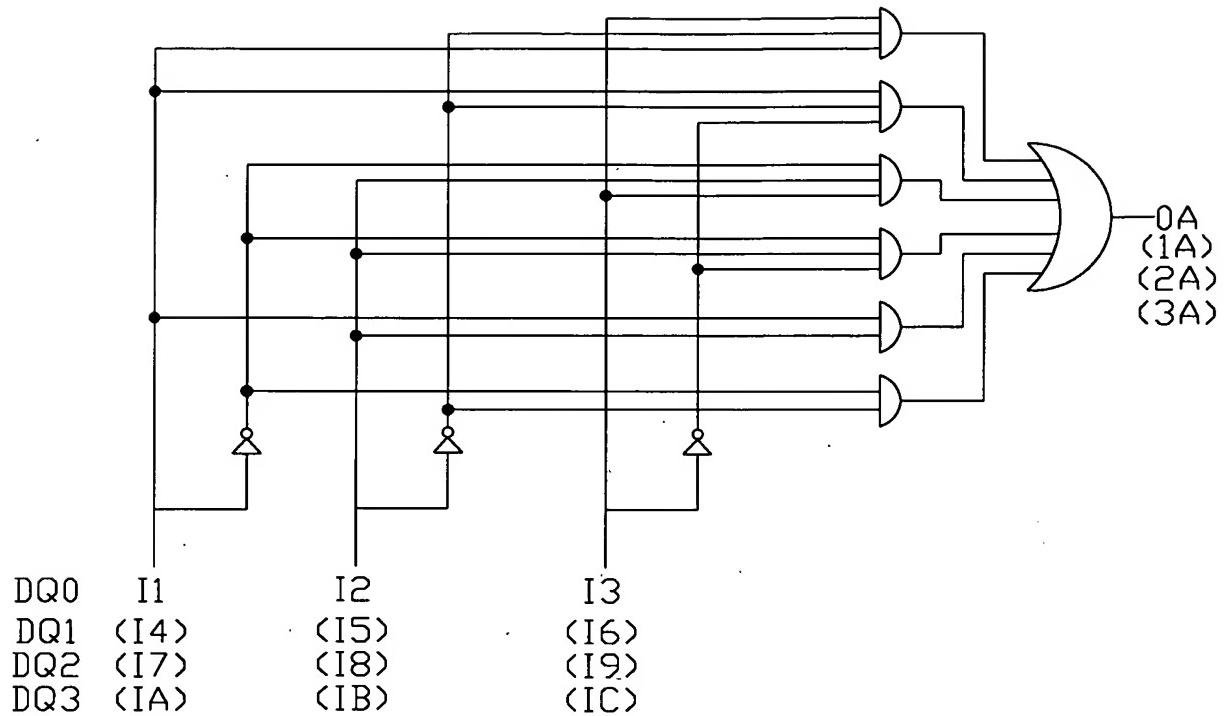


FIG.2A

Input			Output
$I_1(I_4, I_7, I_A)$	$I_2(I_5, I_8, I_B)$	$I_3(I_6, I_9, I_C)$	$0A(1A, 2A, 3A)$
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

FIG.2B

Output						Corresponding results
C ₀	C ₁	C ₂	C ₃	C ₄	C ₅	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I ₂ (second memory)occurs error
1	1	1	0	0	0	I ₁ (first memory)occurs error
0	1	0	1	0	1	I ₃ (third memory)occurs error
0	0	1	0	1	1	0A(first compare circuit) occurs error

FIG.3A

Output						Corresponding results
C ₆	C ₇	C ₈	C ₉	C _A	C _B	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I ₅ (second memory)occurs error
1	1	1	0	0	0	I ₄ (first memory)occurs error
0	1	0	1	0	1	I ₆ (third memory)occurs error
0	0	1	0	1	1	1A(second compare circuit) occurs error

FIG.3B

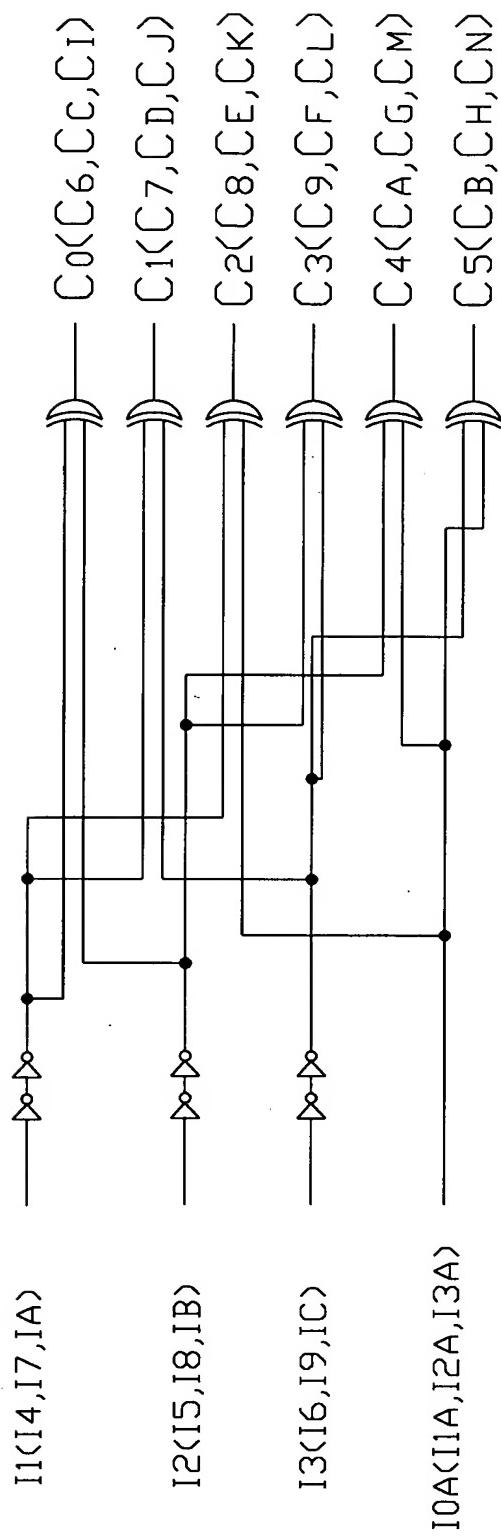
Output						Corresponding results
C _c	C _D	C _E	C _F	C _G	C _H	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I8(second memory)occurs error
1	1	1	0	0	0	I7(first memory)occurs error
0	1	0	1	0	1	I9(third memory)occurs error
0	0	1	0	1	1	2A(third compare circuit) occurs error

FIG.3C

Output						Corresponding results
C _I	C _J	C _K	C _L	C _M	C _N	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I8(second memory)occurs error
1	1	1	0	0	0	I7(first memory)occurs error
0	1	0	1	0	1	I9(third memory)occurs error
0	0	1	0	1	1	3A(fourth compare circuit) occurs error

FIG.3D

FIG. 4



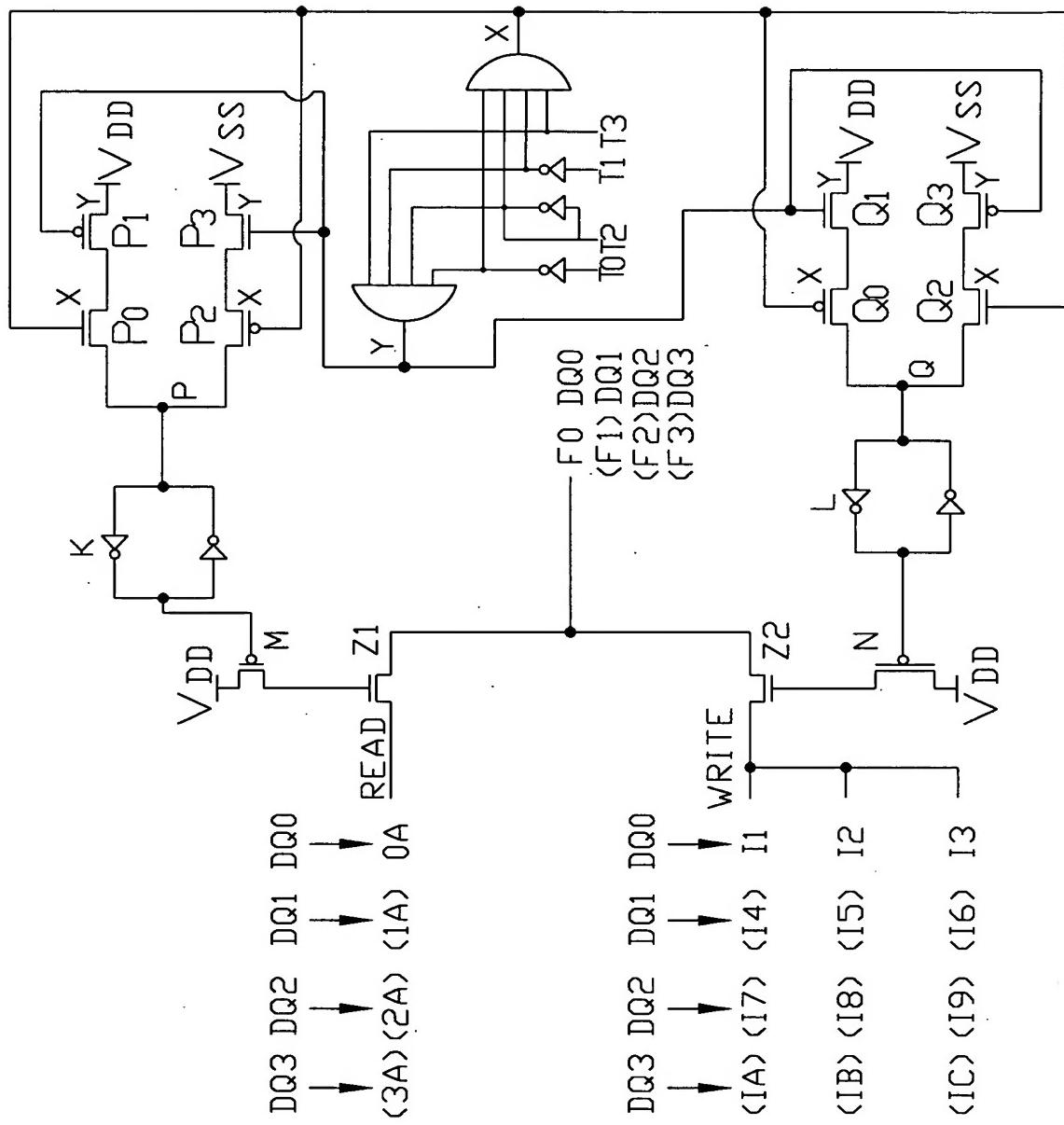


FIG.5

FIG.6
FIG.6A
FIG.6B

X	Y	X'	Y'	X"	Y"	P	Q	left K	left L	Memory read/write
0	0	—	—	—	—	—	—	—	—	No any Read/Write
0	1	—	—	—	—	0	1	1	0	Write
0	0	0	1	—	—	0	1	1	0	After Write, X=0 Y=0 turned up
0	0	0	0	0	1	0	1	1	0	After Write,hold X=0 Y=0
1	0	0	1	—	—	1	0	0	1	After Write,Read turned up immediately
0	0	1	0	0	1	0	0	1	1	After Write,Read turned up immediately and then X=0 Y=0 turned up
0	1	1	0	0	1	0	1	1	0	After Write,Read turned up and then Write turned up
1	0	1	0	0	1	0	0	0	1	After Write,hold Read
0	1	0	1	—	—	0	1	1	0	After Write,Write turned up again
0	0	0	1	0	1	0	1	1	0	After Write,Write turned up again and then X=0 Y=0 turned up
1	0	0	1	0	1	0	0	0	1	After Write,Write turned up again and then Read turned up
1	0	—	—	—	—	1	0	0	1	Read
0	0	1	0	—	—	1	0	0	1	After Read, X=0 Y=0 turned up
0	0	0	0	1	0	1	0	0	1	After Read,hold X=0 Y=0
0	1	1	0	—	—	0	1	1	0	After Read,Write turned up

FIG.6A

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X=Y
Y=X

$$Y' \equiv Y(t-1)$$

$$Y'' = Y(t-2)$$

FIG. 6B